
Intergroup Switch and System Clock Generator Diagnostic (LD 39)

The Intergroup Switch (IGS) and System Clock Generator (SGC) diagnostic (LD 39) applies to the SL-1 XN equipped with Generic X11. The program is used to maintain Peripheral Signaling (PS), Intergroup Switch (IGS) or System Clock Generator (SCG) cards.

ISR messages

ISR0000	Program identifier.
ISR0001	Illegal character in command.
ISR0002	Wrong number of input parameters.
ISR0003	Illegal command.
ISR0004	Group out-of-range.
ISR0010	Command ignored as an active input device would be disabled.
ISR0015 loop	Loop specified does not respond. During ENPS command, an attempt is made to reenale all loops associated with peripheral signaling card.
ISR0018	Only one DISI IGS allowed at a time.
ISR0020	Specified peripheral signaling card out-of-range.
ISR0021	Specified peripheral signaling card does not respond.
ISR0022	Specified peripheral signaling card already enabled.
ISR0025	Cannot determine which CPU is active.

ISR0026 p	A peripheral signaling interrupt fault is present. p identifies the faulty card.
ISR0027	A fault in the outgoing signaling circuitry has been detected on the peripheral signaling card being examined.
ISR0029	Customer does not exist.
ISR0033	System clock must be switched before proceeding.
ISR0040	Intergroup switch out-of-range.
ISR0041	Intergroup switch specified is not responding.
ISR0042	Intergroup switch is already enabled.
ISR0043	DISI IGS command completed.
ISR0050	System clock generator specified is out-of-range.
ISR0051	SCG specified is not responding.
ISR0052	SCG specified is already enabled.
ISR0054	Idle CPU must be switched in to replace the present clock.
ISR0060 n	SCG n cannot be switched in to replace the present clock. Action: If clock controller, go to Input/Output guide, LD 60.
ISR0061 n	SCG n cannot be switched in because loss of service will result to the peripheral signaling cards specified. List is composed of the numbers of the specified peripheral signaling cards affected. Action: If clock controller, go to Input/Output guide, LD 60.
ISR0062 n	Software and hardware status of SCG n do not match Action: If clock controller, go to Input/Output guide, LD 60.
ISR0070	Specified equipment could not be enabled because of the CPU bus extender to the network shelf is disabled.
ISR0071	Supposed to freerun, but H/W is tracking on primary.
ISR0072	Supposed to freerun, but H/W is tracking on secondary.
ISR0073	Supposed to track on primary, but H/W is tracking on secondary.

ISR0074	Supposed to track on primary, but H/W is freerun.
ISR0075	Supposed to track on secondary, but H/W is freerun.
ISR0076	Supposed to track on secondary, but H/W is freerun.
ISR0077	Tracking rejected. Reference primary is not specified.
ISR0078	Tracking rejected. Reference secondary is not specified.
ISR0079 loop	Loop is disabled. No action is taken.
ISR0080	SL-1NT cannot use LD 39.
ISR0100	An invalid error message was received from the clock controller.
ISR0101	Superloop on the specified PS card, use LD 32 ENPS/DSPS commands.
ISR0102	Loop is a Digital Trunk Interface or Primary Rate Interface. Action: Use LD 60.
ISR0104	BRSC{-}PRI B-Ch connection is using the IGS being disabled. Cannot complete DISI command. Action: Use the DIS IGS command or disconnect the nail-up by disabling the BRSC in LD 32 first.
ISR0105	A TCON connection is using the IGS being disabled. Cannot complete DISI command. Action: Use the DIS IGS command or disconnect the TCON first.
ISR0106 G S	Clock found in Group G side S is different from its address found during system initialization. No attempt to overwrite the address found during initialization.
ISR0107 S G	More than one clock has been found in side S. The information on Group of the extra clock(s) is provided.
ISR0108 S	No clock has been found in side S.